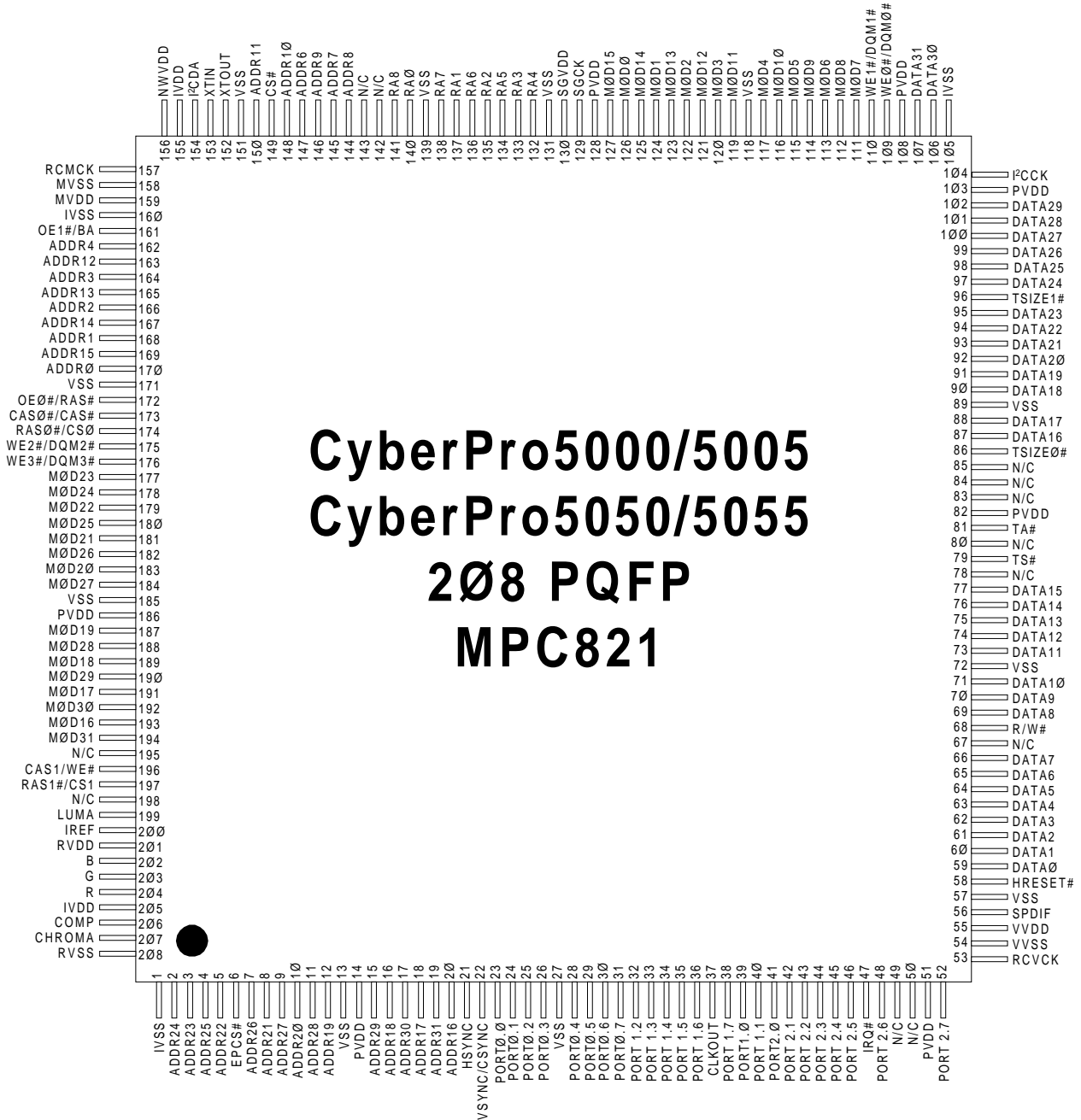
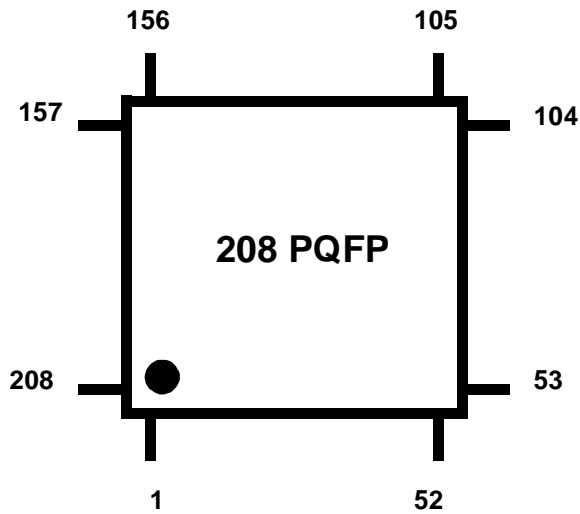


# CyberPro 5000/5005/5050/5055 Pinout Diagram

Host Interface: Motorola MPC 821



**CyberPro5000/5005/5050/5055  
Motorola MPC821 Pinout Table**



**Table 6: MPC821 Pinout**

PIN#	MPC821
24 *	PORT0.1
25	PORT0.2
26	PORT0.3
27	VSS
28	PORT0.4T
29	PORT0.5
30	PORT0.6
31 *	PORT0.7
32	PORT1.2
33	PORT1.3
34	PORT1.4
35	PORT1.5
36	PORT1.6
37	CLK
38	PORT1.7
39	PORT1.0
40	PORT1.1
41	PORT2.0
42	PORT2.1
43 *	PORT2.2
44 *	PORT2.3
45 *	PORT2.4
46	PORT2.5
47	IRQ#
48 *	PORT2.6
49	N/C
50	N/C
51	PVDD
52 *	PORT2.7
53	RCVCK
54	VVSS
55	VVDD
56 *	SPDIF
57	VSS
58	HRESET#
59	DATA0
60	DATA1
61	DATA2
62	DATA3
63	DATA4
64	DATA5

**Table 6: MPC821 Pinout**

PIN#	MPC821
1	IVSS
2	ADDR7
3	ADDR8
4	ADDR6
5	ADDR9
6	VC
7	ADDR5
8	ADDR10
9	ADDR4
10	ADDR11
11	ADDR3
12	ADDR12
13	VSS
14	PVDD
15	ADDR2
16	ADDR13
17	ADDR1
18	ADDR14
19	ADDR0
20	ADDR15
21	HSYNC
22	VSYN/CSYN
23 *	PORT0.0

**Table 6: MPC821 Pinout**

PIN#	MPC821
65	DATA6
66	DATA7
67	N/C
68	R/W#
69	DATA8
70	DATA9
71	DATA10
72	VSS
73	DATA11
74	DATA12
75	DATA13
76	DATA14
77	DATA15
78	N/C
79	TS#
80	N/C
81	TA#
82	PVDD
83	N/C
84	N/C
85	N/C
86	TSIZE0
87	DATA16
88	DATA17
89	VSS
90	DATA18
91	DATA19
92	DATA20
93	DATA21
94	DATA22
95	DATA23
96	TSIZE1
97	DATA24
98	DATA25
99	DATA26
100	DATA27
101	DATA28
102	DATA29
103	PVDD
104	I <sup>2</sup> CCK
105	IVSS
106	DATA30
107	DATA31

**Table 6: MPC821 Pinout**

PIN#	MPC821
108	PVDD
109	WE0#/DQM0#
110	WE1#/DQM1#
111	M0D7
112	M0D8
113	M0D6
114	M0D9
115	M0D5
116	M0D10
117	M0D4
118	VSS
119	M0D11
120	M0D3
121	M0D12
122	M0D2
123	M0D13
124	M0D1
125	M0D14
126	M0D0
127	M0D15
128	PVDD
129	SGCK
130	SGVDD
131	VSS
132	RA4
133	RA3
134	RA5
135	RA2
136	RA6
137	RA1
138	RA7
139	VSS
140	RA0
141	RA8
142	N/C
143	N/C
144	ADDR23
145	ADDR24
146	ADDR22
147	ADDR25
148	ADDR21
149	ADDR26
150	ADDR20

**Table 6: MPC821 Pinout**

PIN#	MPC821
151	VSS
152	XTOUT
153	XTIN
154	I <sup>2</sup> CDA
155	IVDD
156	NWVDD
157	RCMCK
158	MVSS
159	MVDD
160	IVSS
161	OE1#/BA
162	ADDR27
163	ADDR19
164	ADDR28
165	ADDR18
166	ADDR29
167	ADDR17
168	ADDR30
169	ADDR16
170	ADDR31
171	VSS
172	OE0#/RAS#
173	CAS0#/CAS#
174	RAS0#/CS0
175	WE2#/DQM2#
176	WE3#/DQM3#
177	M0D23
178	M0D24
179	M0D22
180	M0D25
181	M0D21
182	M0D26
183	M0D20
184	M0D27
185	VSS
186	PVDD

**Table 6: MPC821 Pinout**

PIN#	MPC821
187	M0D19
188	M0D28
189	M0D18
190	M0D29
191	M0D17
192	M0D30
193	M0D16
194	M0D31
195	N/C
196	CAS1/WE#
197	RAS1#/CS1
198	N/C
199	LUMA
200	IREF
201	RVDD
202	B
203	G
204	R
205	IVDD
206	COMP
207	CHROMA
208	RVSS

**Note: These pin assignments are preliminary and subject to change.**

\* = GPIO Pins

GPIO pins ( 23, 24, 31, 43, 44, 45, 48, 52, 56 ) are not shown on the pin diagram. These pins can be used as General Purpose I/O pins as long as no functions are destined to them.

Note:

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